Application/Control No. Applicant(s)/Patent Under Reexamination 10/751,081 SCHOTTLER, JOSEPH J. Notice of References Cited Examiner Art Unit Page 1 of 1 2816 Tuan T. Lam **U.S. PATENT DOCUMENTS Document Number** Date Name Classification Country Code-Number-Kind Code MM-YYYY US-Α US-В US-C US-D US-Ε US-F US-G US-Н US-US-US-K US-L US-М FOREIGN PATENT DOCUMENTS **Document Number** Date Country Classification Name Country Code-Number-Kind Code MM-YYYY Ν 0 Ρ Q R s Т **NON-PATENT DOCUMENTS** Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) "Electronic Circuits Discrete and Integrated" by Schilling et al., pages 45-51, 1989

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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